

Docket No.: 60188-448

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

: Customer Number: 20277

Keiichi FUJIMOTO, et al.

: Confirmation Number: 2785

Serial No.: 09/964,480

: Group Art Unit: 2829

Filed: September 28, 2001

: Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT TESTING SYSTEM AND METHOD

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

05/07/2004 STANLEY 00000001 500417 09964480

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